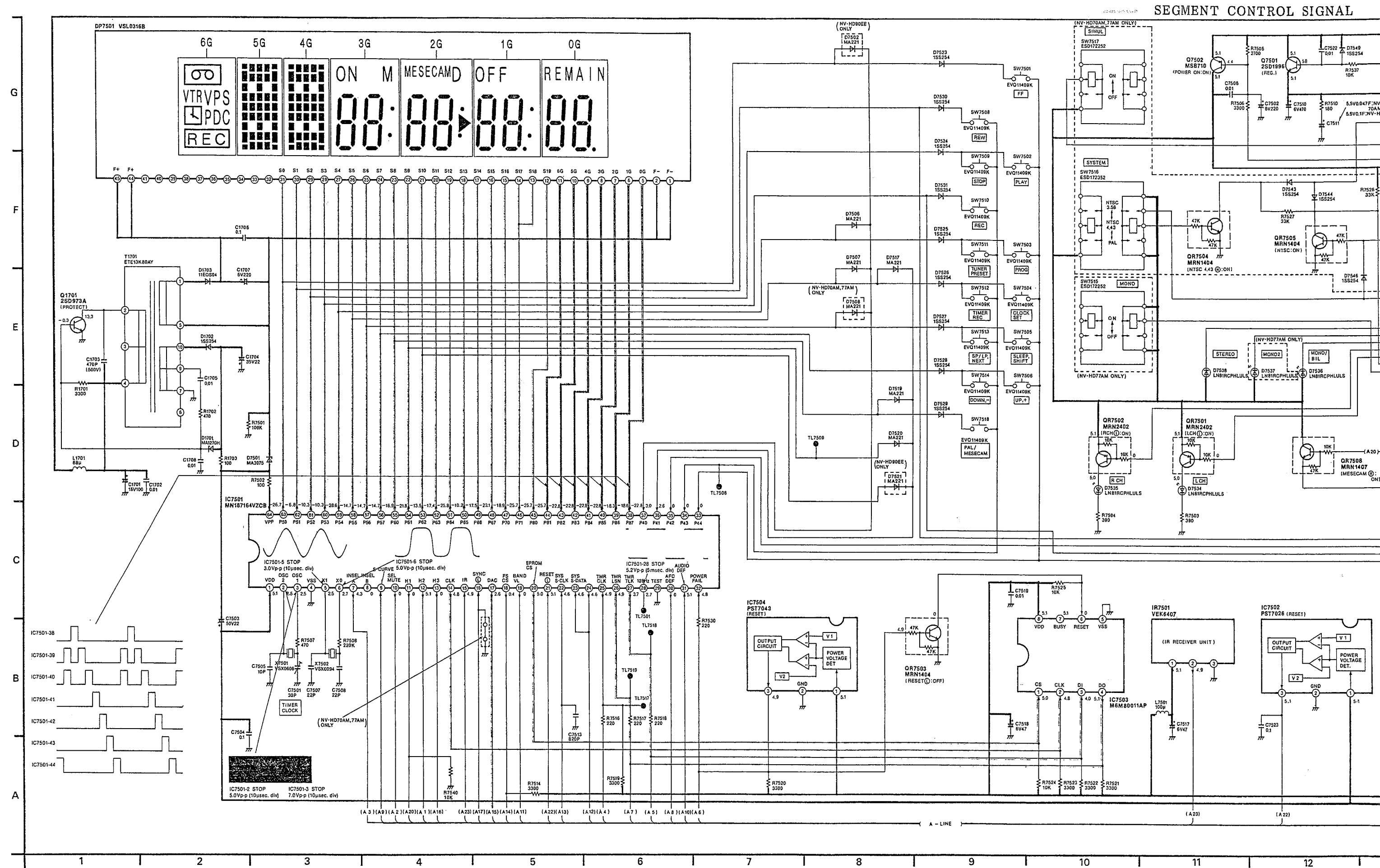


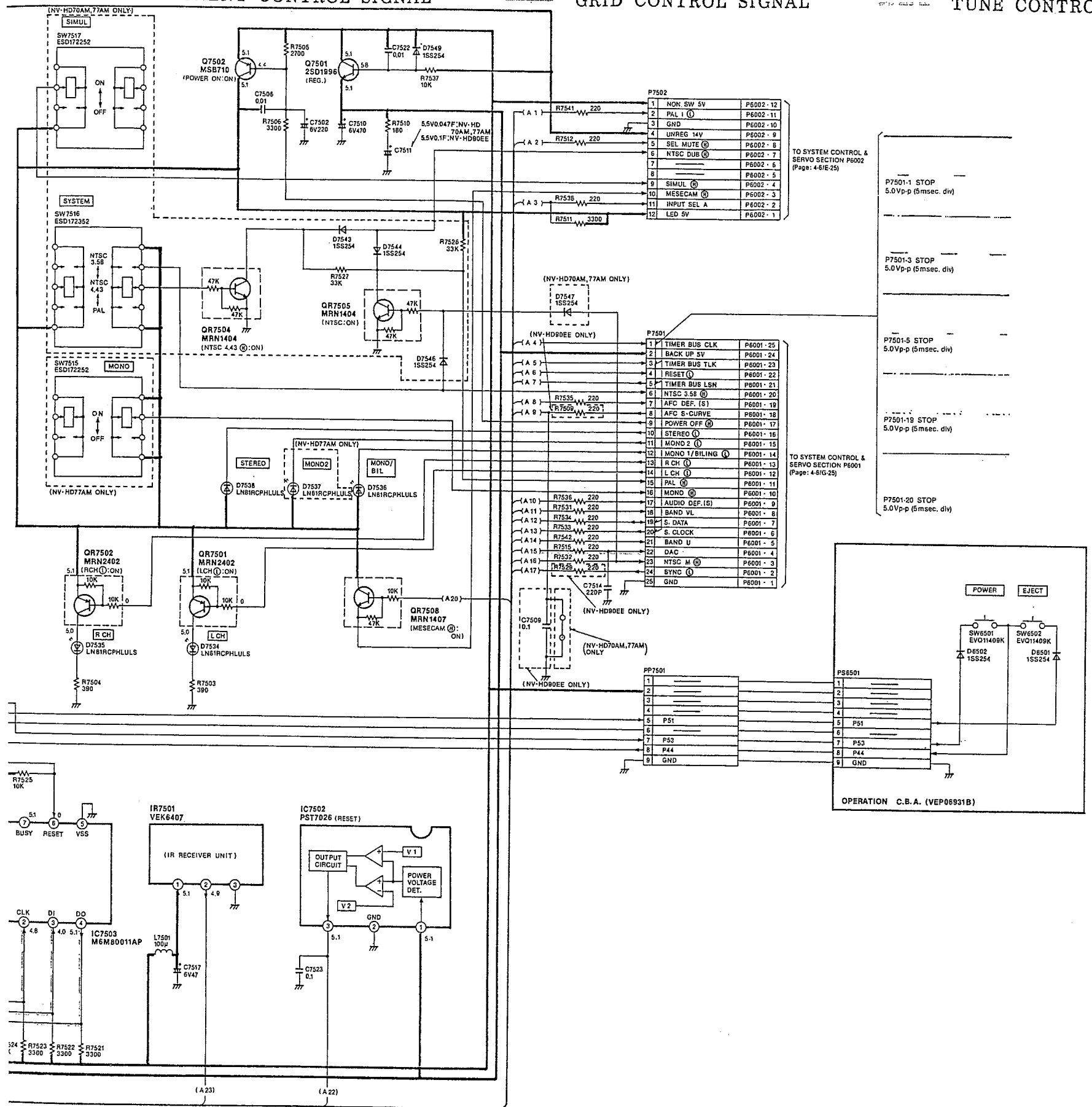
4.8. TIMER & OPERATION SCHEMATIC DIAGRAM



SEGMENT CONTROL SIGNAL

GRID CONTROL SIGNAL

TUNE CONTROL SIGNAL



P7502

1	NON SW 5V	P6002 - 12
2	PAL I (⊖)	P6002 - 11
3	GND	P6002 - 10
4	UNREG 14V	P6002 - 9
5	SEL MUTE (⊖)	P6002 - 8
6	NTSC DUB (⊖)	P6002 - 7
7		P6002 - 6
8		P6002 - 5
9	SIMUL (⊖)	P6002 - 4
10	MESECAM (⊖)	P6002 - 3
11	INPUT SEL A	P6002 - 2
12	LED 5V	P6002 - 1

P7501

1	TIMER BUS CLK	P6001 - 25
2	BACK UP 5V	P6001 - 24
3	TIMER BUS TLK	P6001 - 23
4	RESET (⊖)	P6001 - 22
5	TIMER BUS LSN	P6001 - 21
6	NTSC 3.58 (⊖)	P6001 - 20
7	AFC DEF. (S)	P6001 - 19
8	AFC S-CURVE	P6001 - 18
9	POWER OFF (⊖)	P6001 - 17
10	STEREO (⊖)	P6001 - 16
11	MONO 2 (⊖)	P6001 - 15
12	MONO 1/BILING (⊖)	P6001 - 14
13	R CH (⊖)	P6001 - 13
14	L CH (⊖)	P6001 - 12
15	PAL (⊖)	P6001 - 11
16	MONO (⊖)	P6001 - 10
17	AUDIO DEF (S)	P6001 - 9
18	BAND VL	P6001 - 8
19	S. DATA	P6001 - 7
20	S. CLOCK	P6001 - 6
21	BAND U	P6001 - 5
22	DAC	P6001 - 4
23	NTSC M (⊖)	P6001 - 3
24	BYND (⊖)	P6001 - 2
25	GND	P6001 - 1

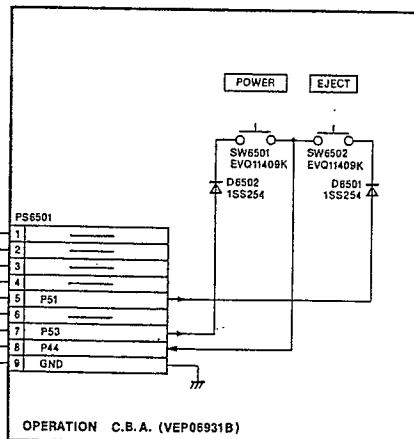
P7501-1 STOP
5.0Vp-p (5msec. div)

P7501-3 STOP
5.0Vp-p (5msec. div)

P7501-5 STOP
5.0Vp-p (5msec. div)

P7501-18 STOP
5.0Vp-p (5msec. div)

P7501-20 STOP
5.0Vp-p (5msec. div)



NOTE: THE MEASUREMENT MODE OF THE DC VOLTAGE ON THIS DIAGRAM IS STOP MODE.

NOTE: DO NOT USE ANY PART NUMBER SHOWN ON THIS SCHEMATIC DIAGRAM FOR ORDERING. WHEN YOU ORDER A PART, PLEASE REFER TO PARTS LIST.